# EECS4315 Mission-Critical Systems

Lecture Notes

Winter 2023

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## Lecture 1 - January 10

**Syllabus & Introduction** 

Safety-Critical Systems Verification vs. Validation Theorem Proving vs. Model Checking TLA+



New Language design ANTLR4 4302 F22 --->model checting. (7A+ -

LOGIC Covered Untimed. poposition/predicates Efrs 3342 . Juppel baic Juil - Imper tempeal temporal baic Juil - Computation temporal - eventual Di EFCS44315 : 2 - eventual Pholds lagic velative - infinitely fren Pholds Notion of the second secon

## Lecture 2 - January 12

**Introduction** 

Safety- vs. Mission-Critical Systems Formal Methods Industrial Standards Verification vs. Validation

Sas > auto-priot / anto - diring traffic Irant / train gato air bag de ployment OPG Buer Gan. La Datario Power Gan. La Vaulagton Matolow Systems. elevator/estatator Impulse dector/pacemaker nuclear power plant/shatdown system





### Mission-Critical vs. Safety-Critical

#### Safety critical

When defining safety critical it is beneficial to look at the definition of each word independently. Safety typically refers to being free from danger, injury, or loss. In the commercial and military industries this applies most directly to human life. Critical refers to a task that must be successfully completed to ensure that a larger, more complex operation succeeds. Failure to complete this task compromises the integrity of the entire operation. Therefore a safety-critical application for an **RTOS** implies that execution failure or faulty execution by the operating system could result in injury or loss of human life.

Safety-critical systems demand software that has been developed using a well-defined, mature software development process focused on producing quality software. For this very reason 3347: theopen points 4315: model checking. the DO-178B specification was created. DO-178B defines the guidelines for development of aviation software in the USA. Developed by the Radio Technical Commission for Aeronautics (RTCA), the DO-178B standard is a set of guidelines for the production of software for airborne systems. There are multiple criticality levels for this software (A, B, C, D, and E).

These levels correspond to the consequences of a software failure:

- Level A is catastrophic (Most Scale)
- Level B is hazardous/severe
- Level C is major
- Level D is minor

Level E is no effect ( es et souce)

Safety-critical software is typically DO-178B level A or B. At these higher levels of software criticality the software objectives defined by DO-178B must be reviewed by an independent party and undergo more rigorous testing. Typical safety-critical applications include both military and commercial flight, and engine controls.

#### **Mission critical**

A mission refers to an operation or task that is assigned by a higher authority. Therefore a mission-critical application for an RTOS implies that a failure by the operating system will prevent a task or operation from being performed, possibly preventing successful completion of the operation as a whole.

### Mission-critical systems must also be developed using well-defined, mature

software development processes. Therefore they also are subjected to the rigors of DO-178B. However, unlike safety-critical applications, missioncritical software is typically DO-178B level C or D. Mission-critical systems only need to meet the lower criticality levels set forth by the DO-178B specification.

Generally mission-critical applications include <u>navigation systems</u>, <u>avionics</u> <u>display systems</u>, and <u>mission command</u> and control.

Source: http://pdf.cloud.opensystemsmedia.com/advancedtca-systems.com/SBS.JanO4.pdf





#### **Building the product right?**



#### **Certifying** Systems: Assurance Cases



Source: https://resources.sei.cmu.edu/asset\_files/whitepaper/2009\_019\_001\_29066.pdf

## Lecture 3 - January 17

## **Introduction, Math Review**

## Model-Based Development TLA+ Logical vs. Programming Operators



- Lab1 released
  - + tutorial videos
  - + problems to solve

#### Software Development Process



(incomplete, ambiguous, contradicting)

charce) {-.. 3 or {...3

- Requirement Elicitation
- 2347: Event-B model 47315: TLAt blele - Blueprints - Not necessarily executable & testable
- IMPLEMENTATION

REQUIREMENT

τf (..) ριφτf (..)

- API Given - Efficient (data structures & algorithms)
- Unit Tests



- Customer's Acceptance
- Recall?

#### **Correct by Construction**



Source: https://audiobookstore.com/audiobooks/failure-is-not-an-option-1.aspx

#### Correct by Construction: Bridge Controller System



#### Correct by Construction: File Transfer Protocol







#### TLA+ Toolbox

TLA + (<u>Temporal Logic of Actions</u>) is a high-level language for modeling programs and systems—especially concurrent and distributed ones. It's based on the idea that the best way to describe things precisely is with simple mathematics.

TLA+ and its tools are useful for eliminating fundamental **design errors**, which are hard to find and expensive to correct in code.

TLA+ is a language for modeling *software* <u>above</u> the code level and *hardware* <u>above</u> the circuit level.

It has an *IDE* (Integrated Development Environment) for writing models and running tools to check them. The tool most commonly used by engineers is the *TLC model checker*, but there is also a proof checker.

TLA+ is based on mathematics and does not resemble any programming language. Most engineers will find *PlusCal*, described below, to be the easiest way to start using TLA+.

I have design language.





#### Logical Operator vs. Programming Operator



Q. Are the  $\wedge$  and  $\vee$  operators equivalent to, respectively, && and || in Java?





## Lecture 4 - January 19

**Math Review** 

## Propositional Logic, Predicate Logic



- Tuesday's lecture recording mossing!
- Lab1 released
  - + tutorial videos
  - + problems to solve

#### **Implication** ≈ Whether a Contract is Honoured





Which of the following expressions are equivalent to  $P \Rightarrow g$ (1) & TE PU Z, & only of PX P <=> g  $\langle \Rightarrow \rangle$ 







### Predicate Logic: Quantifiers

– syntax

base cases in programming






#### Logical Quantifiers: Examples



# Lecture 5 - January 24

**Math Review** 

Logical Quantifications: Proof Strategies Exercises



#### Lab1 Part 2 tutorial videos released

- + ≈ 2 hours
  - \* debugging using labels, error trace, state graph
  - \* PlusCal vs. Auto-Translated TLA+ Predicates
- Optional Textbook for Model Checking and Program Verification
   Logic in Computer Science:
   Modelling and reasoning about systems
   by M. Huth and M. Ryan



How to disprove  $\forall i \bullet R(i) \Rightarrow P(i) ?$ (1) Give a witness/conner-excuple :  $R(\tau) \land \neg R(\tau)$   $e.g. ever there \Rightarrow gives context about <math>\tau_3$  be ease How to disprove  $\exists i \bullet R(i) \land P(i) ? s.t. R(\tau) might to be$ <math>norder(1) show  $\neg R(\tau) (\Rightarrow empty average)$  $norder(2) R(\tau) \land \neg R(\tau) \Rightarrow for all <math>\tau$  satisfying  $R_3$  they dolt satisfy P posed









## Predicate Logic: Exercise 1



Consider the following predicate:

 $\forall \mathbf{x}, \mathbf{y} \bullet \mathbf{x} \in \mathbb{N} \land \mathbf{y} \in \mathbb{N} \Rightarrow \mathbf{x}^* \mathbf{y} > \mathbf{0}$ 

Choose all statements that are correct.





3. It is not a theorem, witnessed by (5, 0).  $(5 \in A \land D \in A)$ (4. It is not a theorem, witnessed by (12, -2). 5. It is not a theorem, witnessed by (12, 13). (7) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (12) (

 $12 \in \mathbb{N} \land [-2 \in \mathbb{N}] \Rightarrow |2 \times -2 > 0 = (\overline{D}, =) |2 \times |3 > 0$ 

Consider the following predicate:  $\forall x, y \bullet x \in \mathbb{N} \land y \in \mathbb{N} \Rightarrow x * y * 0$ 

Choose <u>all</u> statements that are correct.



- An axion is assumed to be true, with no need for proofs. A theorem is a Boolean expression that requires a proof. 4 lemma 6 sub-theorems to help.

## Predicate Logic: Exercise 2

Consider the following predicate:

 $\exists x, y \bullet x \in \mathbb{N} \land y \in \mathbb{N} \land x^* y > 0$ 

Choose all statements that are correct.

- 1. It is a theorem, provable by (5, 4).  $\wedge 5 \neq 4 \in \mathbb{N}$
- 2. It is a theorem, provable by (2, 3).  $\chi$  3. It is a theorem, provable by (-2, -3). [-2  $\in N$ ]
  - 4. It is not a theorem, witnessed by (5, 0).
  - 5. It is not a theorem, witnessed by (12, -2).
  - 6. It is not a theorem, witnessed by (12, 13).

## Logical Quantifications: Conversions

R(x): x ∈ 4315\_class P(x): x receives A+



# Lecture 6 - January 26

**Model Checking** 

Introduction Linear-time Temporal Logic (LTL): Syntax



- Lab1 Part 2 tutorial videos released
  - + Help: Scheduled Office Hours & flexible TA hours
  - + ≈ 2 hours
    - \* debugging using labels, error trace, state graph
    - \* PlusCal vs. Auto-Translated TLA+ Predicates
- <u>Optional</u> Textbook for Model Checking and Program Verification
  - Logic in Computer Science:
    - Modelling and reasoning about systems
  - by M. Huth and M. Ryan

## Use of Model Checking in Industry

Pentium FDIV bug: https://en.wikipedia.org/wiki/Pentium\_FDIV\_bug

The Pentium FDIV bug is a hardware bug affecting the **floating-point unit (FPU)** of the early Intel Pentium processors. Because of the bug, the processor would return <u>incorrect</u> binary floating point results when dividing certain pairs of high-precision numbers.

In December 1994, Intel **recalled** the defective processors ... In its 1994 annual report, Intel said it incurred "**a \$475 million pre-tax charge** ... to recover replacement and write-off of these microprocessors."

In the aftermath of the **bug** and subsequent **recall**, there was a marked increase in the use of formal verification of hardware floating point operations across the **semiconductor industry**. Prompted by the discovery of the bug, a technique ... called "word-level **model checking**" was developed in 1996. Intel went on to use **formal verification** extensively in the development of later CPU architectures. In the development of the Pentium 4, symbolic trajectory evaluation and **theorem proving** were used to **find a number of bugs that could have led to a similar recall incident** had they gone undetected.



Temporal Logic - Syntax : Structure auto-tanslated from Pluslal state graph chosed on (TLA+ preditates) YES ME Ø model ohecker (TLA+; SPIN, Uppal) Cstate explosion) No MF Ø Semantits : meaning TATE OF L> (1) how to express 2, how to check (Bror (3) when the check failed, how to interpret the error trate)



## LTL Syntax: Context-Free Grammar





(1)  $F\phi_1 \Rightarrow \phi_2$  $L_{3}^{(a)} F(\phi_{1} \Rightarrow \phi_{2}) \xrightarrow{(b)} (F\phi_{1}) \Rightarrow \phi_{2}$ 





not what (1) means



# Lecture 7 - January 31

# **Model Checking**

Practical Knowledge about Parsing Operator Precedence Drawing Parse Trees Left-Most Derivation (LMD)



- Lab1 Part 2 tutorial videos released
  - + Help: Scheduled Office Hours & flexible TA hours
  - + ≈ 2 hours
    - \* debugging using labels, error trace, state graph
    - \* PlusCal vs. Auto-Translated TLA+ Predicates
- <u>Optional</u> Textbook for Model Checking and Program Verification
  - Logic in Computer Science:
    - Modelling and reasoning about systems
  - by M. Huth and M. Ryan
- Written Test 1 approaching...
  - Ly WSC O EELS bogra → lab computer @ PPX logra → ellax

#### Parsing: Some Practical Knowledge



> Contex

Assumption: Operator precedence considered first before the CFG.

## Interpreting a Formula: Parse Trees (1)



DOWN:

root -> leaves

## Interpreting a Formula: Parse Trees (1)



DOWN:

root -> leaves



# Interpreting a Formula: Parse Trees (2) $\theta \cdot P \Rightarrow g \Rightarrow y$



PAQ = QAP but different PTs. Given two formula strings fl and fz different spellings. (1) If fl = fz, but fl and fz have the same

MEANS the Grammar TS ambigues.

D part of the input string to force some order of interpretation. 2 parentheses are ownitted in PTS.

#### Interpreting a Formula: Parse Trees (3)



#### Interpreting a Formula: Parse Trees (4)



## Interpreting a Formula: LMD (1)



# Lecture 8 - February 2

# **Model Checking**

# *Comparison: Parse Trees, LMDs, RMDs Deriving Subformulas Labelled Transition System (LTS)*
## Interpreting a Formula: LMD (1)



## Interpreting a Formula: LMD (2)



## Interpreting a Formula: LMD (3)



## Interpreting a Formula: LMD (4)



## Interpreting a Formula: RMD (1)



## Interpreting a Formula: RMD (2)



## Interpreting a Formula: RMD (3)



## Interpreting a Formula: RMD (4)



## Interpreting a Formula: PT vs. LMD vs. RMD









# Context-Free Grammar (CFG): Exercise (optional) Is the following CFG ambiguous?

. . .

Statement  $\rightarrow$  if Expr then Statement if Expr then Statement else Statement Assignment

**Example:** 

if Expr1 then if Expr2 then Assignment1 else Assignment2

## Context-Free Grammar (CFG): Exercise

## Is the following CFG ambiguous?

. . .



**Example:** A Possible Semantic Interpretation?

**if** *Expr1* **then if** *Expr2* **then** *Assignment1* **else** *Assignment2* 



## Context-Free Grammar (CFG): Exercise

## Is the following CFG ambiguous?

. . .



#### Example: A Possible Semantic Interpretation? if Expr1 then if Expr2 then Assignment1 else Assignment2







Lecture 9 - February 7

**Model Checking** 

Examples: LTS Formulation Paths, Unwinding All Possible Paths Path Satisfaction: X, G, F

#### Announcements

- Lab2 released
- WrittenTest1 coming

  - Cover until and mum + some left-over examples ( to be -finished within fast 20 min on Thursday).



## Labelled Transition System (LTS)

**Exercises** Consider the system with a counter *c* with the following assumption:

 $0 \leq \textit{C} \leq 3$ 

M,

deci

 $0 < C_1 \leq 2$ 

3 5 (z 5 5 TACz

v inet:  $C_{1} = 1$  decz

(7= 3

S= {So, SI, Sr, S3 3

(Jo, SI)

(SI, SZ);

(Jz, J3);

(J3, Sz),

(Jz JI).

(JI - Jo) J

 $(\leq 13)$ 

Exertse

Say *c* is initialized 0 and may be incremented (via a transition *inc*, enabled when c < 3) or decremented (via a transition *dec*, enabled when  $c \ge 0$ ).

ML

dec

 $M = (S, \rightarrow)$ 

AL

dec

SS

• **<u>Draw</u>** a *state graph* of this system.

50

terested

• **Formulate** the state graph as an **LTS** (via a triple  $(S, \rightarrow, L)$ ).

<u>Assume</u>: Set P of atoms is:  $\{c \ge 1, c \le 1\}$ 

CARC

TAC

## Labelled Transition System (LTS): Formulation & Paths









### Path Satisfaction: Temporal Operations (1)

- A path satisfies X¢
- if the **next state** (of the "current state") satisfies it.



\*

"Curvent state"

Formulation (over a path)





Path Satisfaction: Temporal Operations (2)

A path satisfies Gp

if the every state satisfies it.



Formulation (over a path)



Path Satisfaction: Temporal Operations (3)

A path satisfies (F)

if some future state satisfies it.



Formulation (over a path)



## Lecture 10 - February 9

**Model Checking** 

## Path Satisfaction vs. Model Satisfaction Unary Temporal Operators: X, G, F

## Announcements

- <u>Lab1</u> solution coming soon!
- <u>Lab2</u> released
- WrittenTest1 guide & example questions released
  - + Verify EECS account on a WSC machine
  - + Verify PPY account and Duo Mobile on eClass
- Review session on Monday? 1pm or 2pm?









### Path Satisfaction: Temporal Operations (1)



#### **Model** Satisfaction

#### Given:

- Model M = (S, →, L)
- State s ∈ S
- LTL Formula 🔶

 $M_{S} \models \phi \text{ iff for every path } \pi \text{ of } M \text{ starting at } s, \pi \models \phi.$ 

Formulation (over all paths) model satisfaction T = S -> total path w.v.t. W

 $S \oplus \phi \iff \forall \pi \cdot \pi \text{ starts with } S \Rightarrow \pi \neq \phi$ 

(1) To prove  $S \models \phi$ , need to show for every possible path TL, 2, To disprove  $S \models \phi$ , proved a wretheress  $TL = S \rightarrow \cdots$ ,  $TL \neq \phi$ .

How to prove vs. disprove M,  $s \models \phi$ ?

#### Model vs. Path Satisfaction: Exercises (1.1)



#### Model vs. Path Satisfaction: Exercises (1.2)



Exercise: What if we change the LHS to si?

#### Model vs. Path Satisfaction: Exercises (2.1)


#### Model vs. Path Satisfaction: Exercises (2.2)



Exercise: What if we change the LHS to si?

#### Model vs. Path Satisfaction: Exercises (3.1)



### Model vs. Path Satisfaction: Exercises (3.2)



Exercise: What if we change the LHS to si?

#### Model vs. Path Satisfaction: Exercises (4.1)



**Exercise**: What if we change the <u>LHS</u> to  $\pi^2$ ?

## Model vs. Path Satisfaction: Exercises (4.2)



# Sunday, February 12

# **Written Test 1 Review**





 $F P \land (G Q => |U r)$   $T_{S} = 10 r$ >Unard temporal K.F.G WTI: perfers con this be strick temporal the LHS of U op? of temporal the LHS of U op? Binary temporal  $V_{z} W_{z} R$ Unay Prop. F [2] ∧ (G [3] [2] [2] [3]) S altomatively: rot = right has bull $<math>(G_{\mathcal{R}} \Rightarrow r) \cup S$  than  $\cup$ . ٨ V an operativ precedence, Assume: guestions will vet veq. a decision on the association of  $\Rightarrow$ ,  $\chi$ . than U



## Lecture 11 - February 28

**Model Checking** 

Path Satisfaction: Nested LTL Operators FG vs. F => FG

## **Announcements**

- Released: WrittenTest1, Lab2 solution
- To be released:
  - + ProgTest1 Guide (by the end of Wednesday)
  - + **ProgTest1** practice questions (by Thursday class)

- I~Zalgorithms L - conditionals, loops, tuples - assertions (postcondition)

> progtest1





 $\underbrace{\mathbf{S}}_{i} \models \mathbf{FG} \ \mathbf{\phi} \qquad \begin{array}{c} \mathcal{T}_{i} & \mathcal{T}_{j} & \mathcal{T}_{j} \\ \mathcal{S}_{i} & \mathcal{S}_{i} & \mathcal{S}_{i} \\ \mathcal{S}_{i$ 

**<u>Q. Formulate</u>** the above nested pattern of LTL operator.

Q. How to prove the above nested pattern of LTL operators?
 ★ ① Ensider all path patterns starting from S → (minutegrith state)
 \*\*(2) And such i \*\* 3) each state subsequent to Ith state surplies
 Q. How to disprove the above nested pattern of LTL operators?
 ★ ① Find a witness TI = S → ...
 ★ \*\*\* 3) there's one subsequent
 \*\*\*\* 3) there's one subsequent

## Path Satisfaction: Exercises (5.1)









**Q.** How to **disprove** the above nested pattern of LTL operators?

# Lecture 12 - March 2

## **Model Checking**

# Path Satisfaction: Nested LTL Operators $F \phi_1 \Rightarrow FG \phi_2$



Each path  $\pi$  starting with s is s.t. if eventually  $\phi_1$  holds on  $\pi$ , then  $\phi_2$  eventually holds on  $\pi$  continuously.

**<u>Q.</u>** Formulate the above nested pattern of LTL operators.

\* $\forall \pi \cdot \pi = S \rightarrow \cdots \Rightarrow (\exists i_1 \cdot i_2 \cdot i_2 \cdot d_1)$ ( $\exists i_1 \cdot i_2 \cdot d_1 \wedge \pi^{i_1} \models \phi_1$ ) ( $\exists i_2 \cdot i_2 \rightarrow | \wedge (\forall j \cdot j \rightarrow i_2 \Rightarrow))$  $\pi^{i_1} \models \phi_2$ )

Q. How to prove the above nested pattern of LTL operators? D Consider all path patterns  $\bigcirc a T \Rightarrow T b F \Rightarrow \_ C = \Rightarrow T$ Q. How to disprove the above nested pattern of LTL operators? D Find a witness path  $\bigcirc T \Rightarrow F$ .





## Lab2 Solution: getAllSuffixes (V2: Tuple of Tuples)



```
MODULE getAllSuffixes_v2 ------
EXTENDS Integers, Sequences, TLC
CONSTANT input
                                                                        input: [23, 46, 69]
result: we k to
ASSUME Len(input) > 0
(*
--algorithm getAllSuffixes_v2 {
    variable result = input, postfixSoFar = <<>>, i = Len(input) - 1;
                                                                          [[23, 46, 69],
    Ł
        postfixSoFar := <<input[Len(input)]>>;
                                                                            [46, 69],
        result[Len(input)] := postfixSoFar;
                                                                          \-[69]]
        while (i > 0) {
            postfixSoFar := <<input[i]>> \o postfixSoFar;
            result[i] := postfixSoFar;
            i := i - 1;
        };
        assert \A j \in 1..Len(input): Len(result[j]) = Len(input) - j + 1;
        assert A j \in 1... en(result): (A k \in 1... en(result[j]): result[j][k] = input[j - 1 + k]);
                                                                      an item to some result tuple
}
*)
                                           (Lon (result [1]))
```



Assertion: explicit about the variables that can be used.





## Lecture 13 - March 14

**Model Checking** 

Model Satisfaction: Nested LTL Operators GF  $\phi$ , GF  $\phi \Rightarrow$  GF  $\phi$ 

LTL Operators: Until, Weak Until, Release

#### **Announcements**

- ProgTest1 result to be released by Friday
- Lab3 to be released by the end of Thursday



**S GF b** *infinitely* **T**: **(a) (b) (c) (c)** 

Q. Formulate the above nested pattern of LTL operator. Let  $\frac{\pi}{2}$ 

**Q.** How to prove the above nested pattern of LTL operators?

Q. How to disprove the above hested pattern of LTL operators? \* Give a witness path TL Give A witness state on TL, say S' +\*\*\* velocitive to s' on TL,



## Model Satisfaction: Exercises (6.1)












### Lecture 14 - March 16

**Model Checking** 

LTL Examples: Until, Weak Until, Release Formulating Natural Language in LTL

# Announcements 6:45 vides

- Mar 23 class?
- ProgTest1 result to be released by the end of Friday
   Lab3 released PRVE P.M. ellos logm.
- WrittenTest2 example questions to be released
  - Review Q&A session: 7pm on Sunday, March 19?



#### Model Satisfaction: Exercises (7.1)



#### Model Satisfaction: Exercises (7.2)





## Formulating Natural Language in LTL (2.1) $G\phi \equiv \neg F \neg \phi$ $F\phi \equiv \neg G \neg \phi$

Natural Language:

It's impossible to reach a state

where the system is started but not ready.

Assumed atoms:

- started
- ready

LTL Formulation G(7(started ~ ready)) G(7started v ready) > Gr (started => read 2/)

7F ( started ~ rready)

## Formulating Natural Language in LTL (2.2) no stanction

#### Natural Language:

Whenever a request is made,

it will be acknowledged eventually.

Assumed atoms:

- requested
- acknowledged



LTL Formulation

#### Formulating Natural Language in LTL (2.3)

#### Natural Language:

An elevator traveling upwards at the 2nd floor

- does not change its direction
- when it has passengers wishing to to to the 5th floor.

HoorZ A batton Fressed 5 => (drection by () floors)

Assumed atoms: LTL Formulation

- floor2, floor5
- directionUp
- buttonPressed5

## Sunday, March 19

## Written Test 2 Review



- [syntax] L's derviable ton granna, L's formal meaning - Correct or not? SUt ns SU(trG(75))  $G\left(\frac{\phi_{1}}{2},\frac{\eta_{2}}{2}\right)$ Compare to GIF \$







## Lecture 15 - March 28

**Program Verification** 

Stronger vs. Weaker Assertions Total vs. Partial Correctness

## Labs grace period until Labs grace period until

#### **Announcements**

- Bonus Opportunity Course Evaluation
- ProgTest1: Echo (eMail, Zoom); Jackie (Office Hour)

Ly one side only; put anything you like Ly comprise-typed 3, 10pt

- Lab3 due tomorrow
- ProgTest2 / · ·
- Final Exam: Review Q&A Sessions

olata sheet



## **Program Verification**

#### **Correctness - Motivating Examples**







#### Program Correctness: Example (1)



#### Program Correctness: Example (2)





## **Program Verification**

#### Hoare Triple and Weakest Precondition



## Lecture 16 - March 30

**Program Verification** 

Weakest Precondition (WP) WP Rules

#### Announcements

- Lab3 due tomorrow
  ProgTest2

Sheel of difficient 2 EEUSpelliozz











## **Program Verification**

### **Rules of wp Calculus**





#### Correctness of Programs: Assignment (1)


## Correctness of Programs: Assignment (2)



$$\{??\} \times := x + 1 \{x = 23\}$$



#### **Program Correctness:** Revisiting Example (1)



### **Program Correctness:** Revisiting Example (2)



## **Rules of Weakest Precondition: Conditionals**

# wp(if B then S1) else S2 end, R) $B \Rightarrow wp(S1, R)$ S









## **Correctness** of Programs: **Conditionals**





## **Correctness** of Programs: Sequential Composition



# Lecture 17 - April 6

**Program Verification** 

Contracts of Loops: Invariant vs. Variant Correctness of Loops

#### **Announcements**

- Lab4 released
- Exam guide released

Program Verification

- Predicates : Stronger ⇒ Weaker - Hoave Traple  $\{0, 3\}$  S  $\{R\}$  $\langle \Rightarrow 0, 3 \rangle up(S, R)$ Thow we calculation & Rore , one (a) of them else = { just: limited } = { --- 3 lepter 3. 5 = { ... - 3



# **Program Verification**

## **Contracts of Loops**





Autput: index i s.t. input [i] is max.

- Evercise. Write an assertion for the post condition.

- Exercise Z: <u>loop invaitant</u>. L> Hant: loop counter Hant: inclusive of <u>j</u> or not?







### **Contracts** of Loops: Violations

Assume: Q and R are true



## **Contracts of Loops: Visualization**





# **Program Verification**

## **Correctness Proofs of Loops**

## **Correct Loops: Proof Obligations**



### **Correct Loops: Proof Obligations**



Example



I hope you enjoyed learning with me A All the best to you ?